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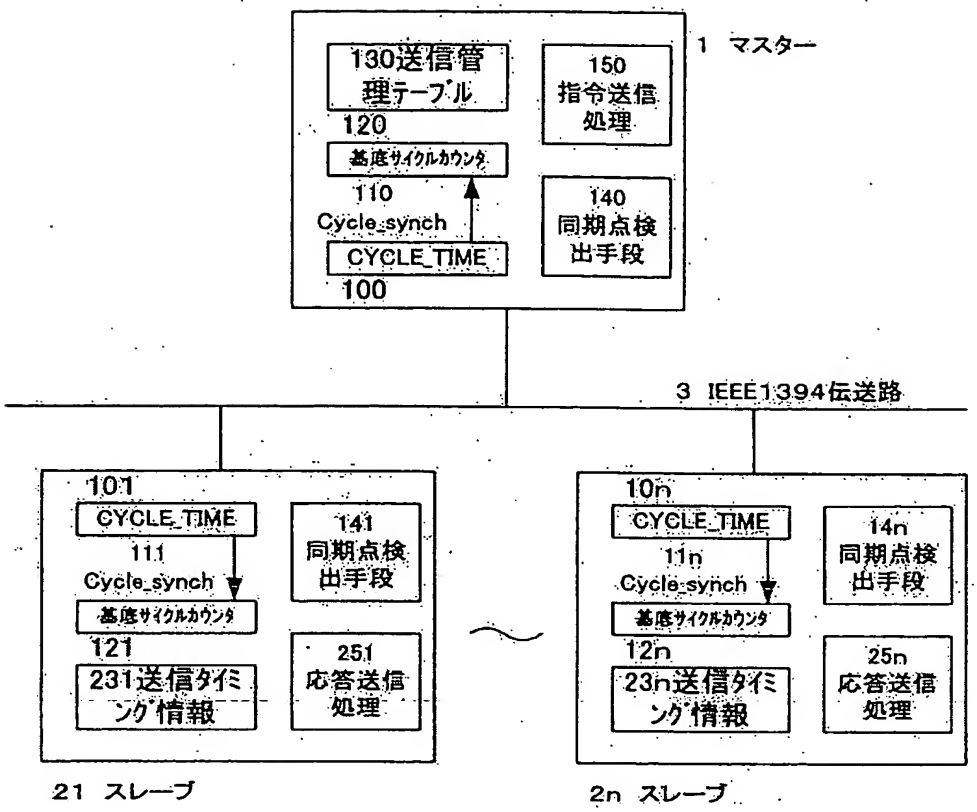
Fig. 2

~~[図1]~~



~~[図2]~~ Fig. 2

マスター送信管理テーブル130

①	全サイクル数	m+1		
②	サイクルカウンタ	0	1	m
③	送信指令数	2	2	2
④	送信先スレーブNo.	#1 #2	#3 #4	#n−1 #n

スレーブ送信タイミング情報 231

⑤	全サイクル数	m+1
⑥	指令サイクル	0
⑦	応答サイクル	0

スレーブ送信タイミング情報 23n

⑤	全サイクル数	m+1
⑥	指令サイクル	m
⑦	応答サイクル	m

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Fig. 3

~~[図3]~~

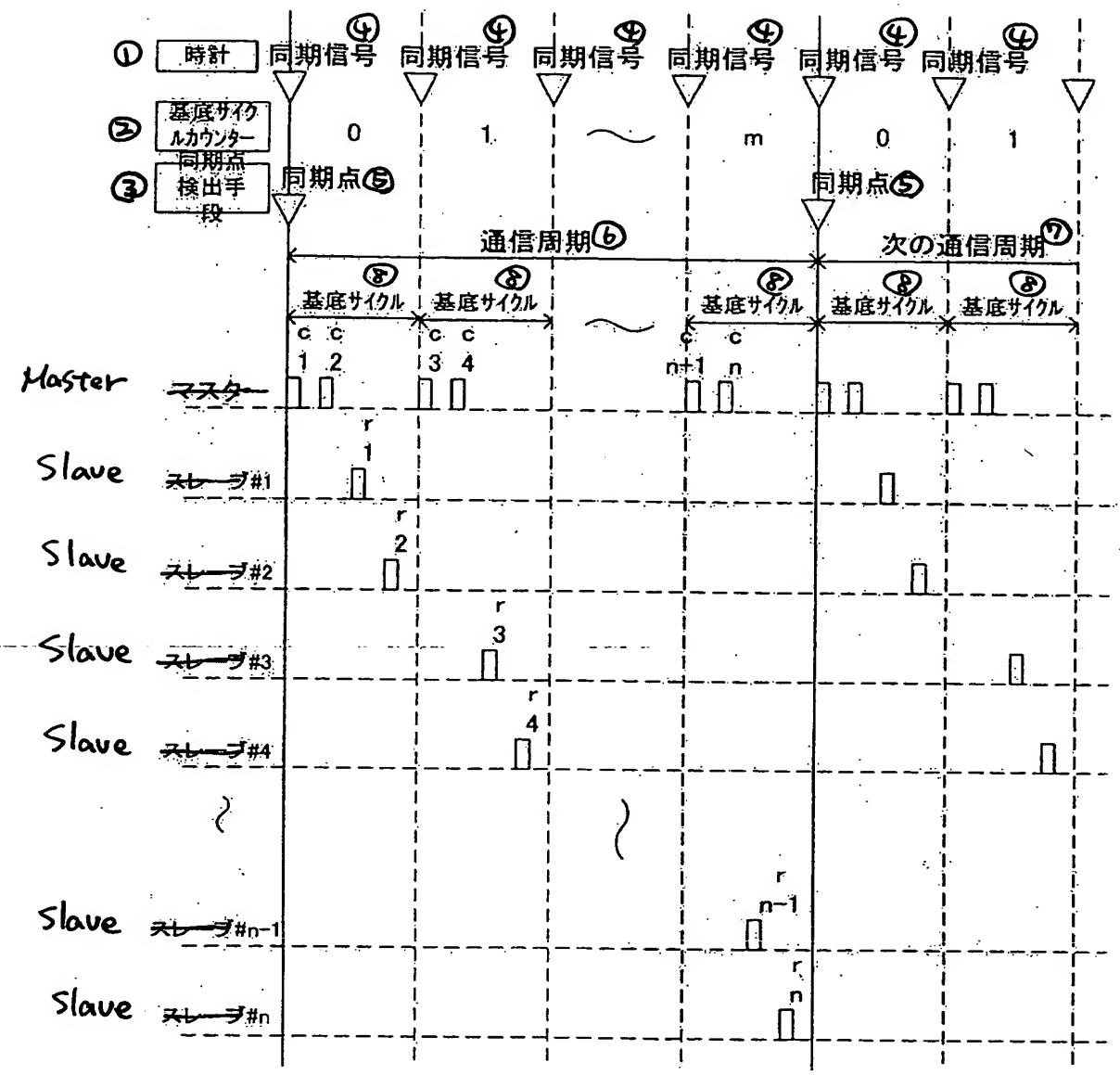

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Fig. 4

~~[図4]~~

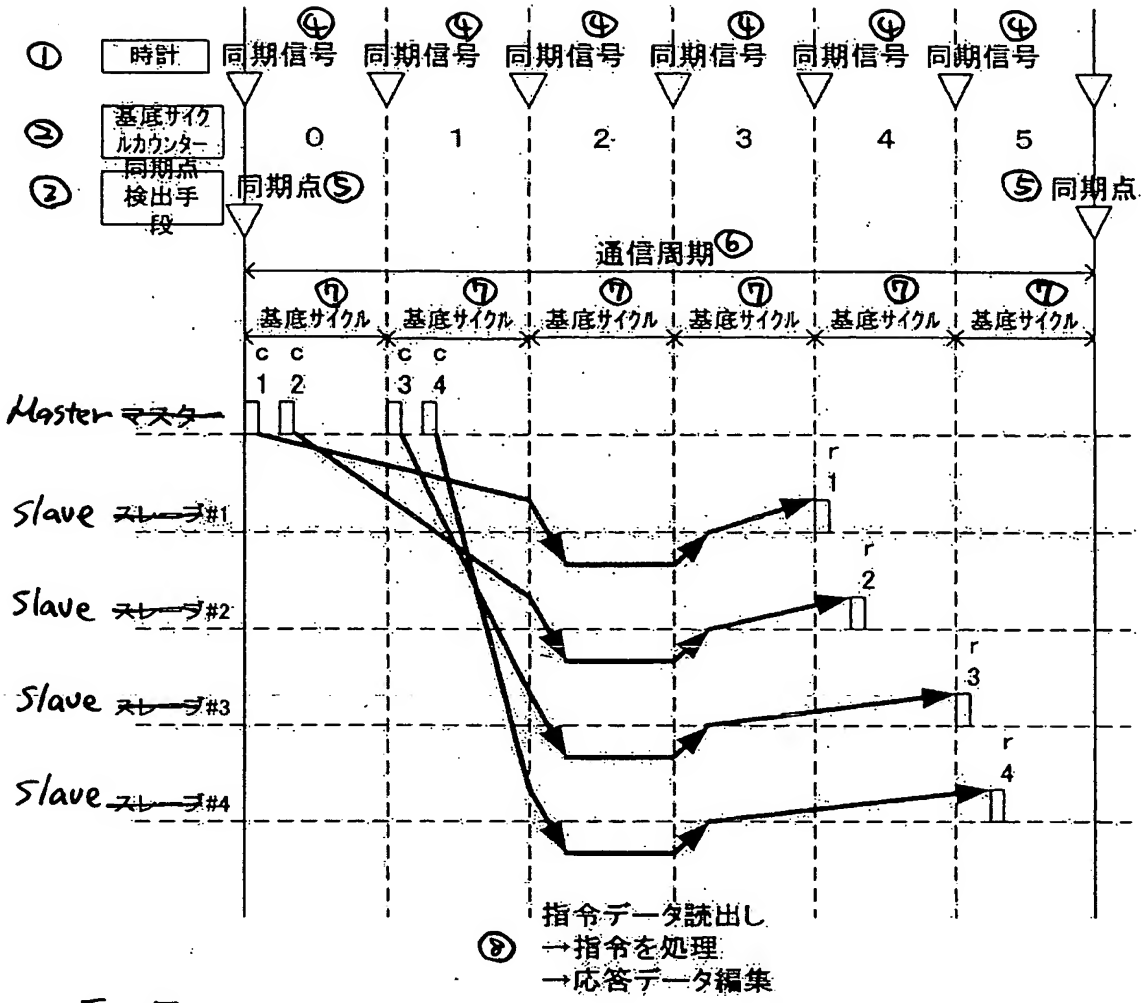

Fig. 5

~~[図5]~~

second_count (7bit)	cycle_count (13bit)	cycle_offset (12bit)

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Fig. 6

~~[図6]~~

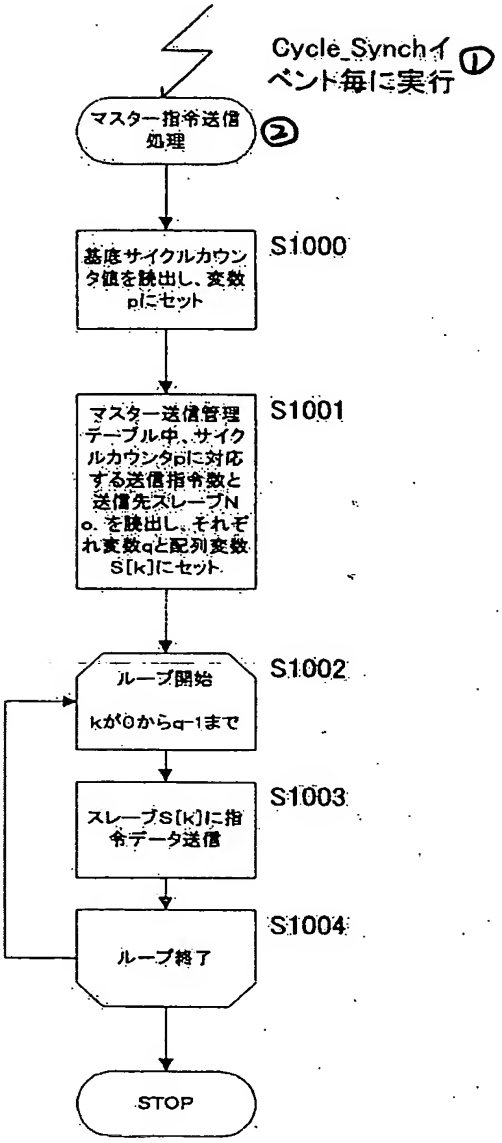

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Fig. 7

~~[図7]~~

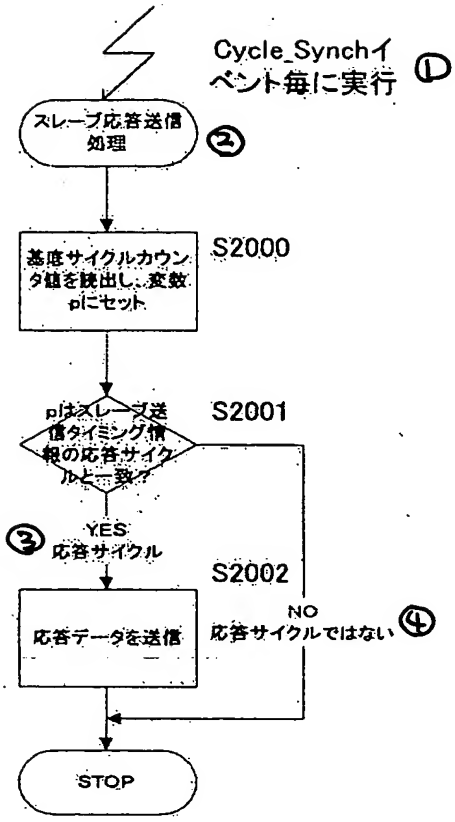

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Fig. 8
~~[図8]~~

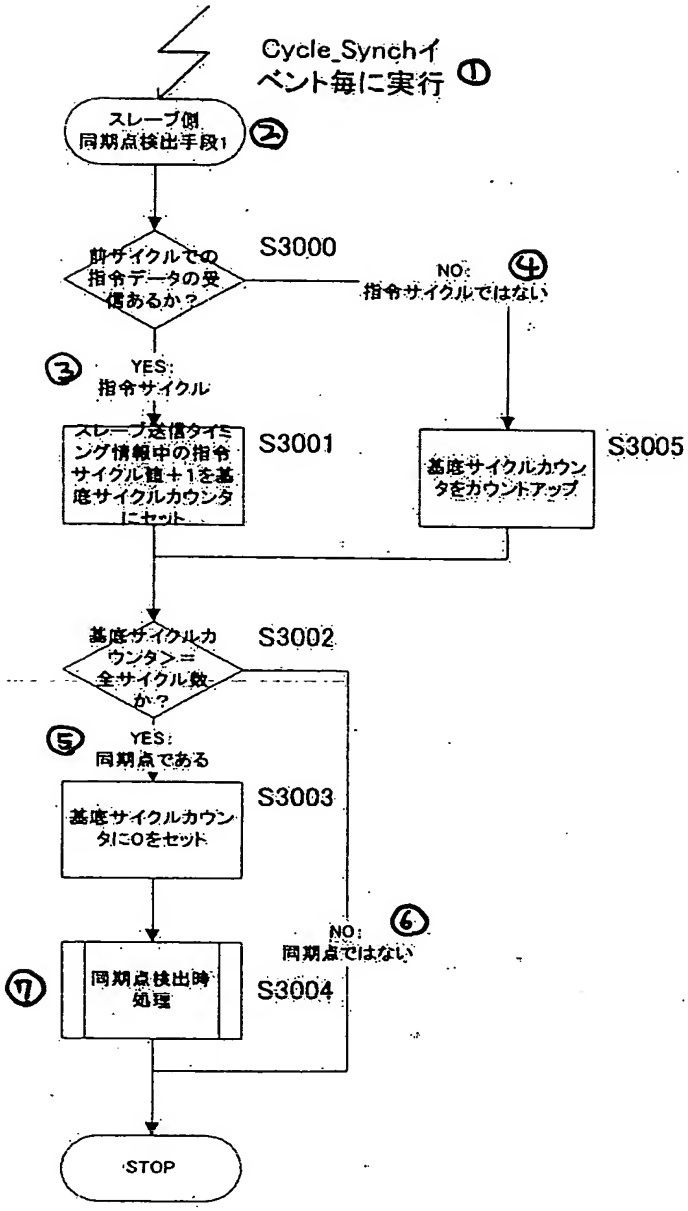

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Fig.9

~~[図9]~~

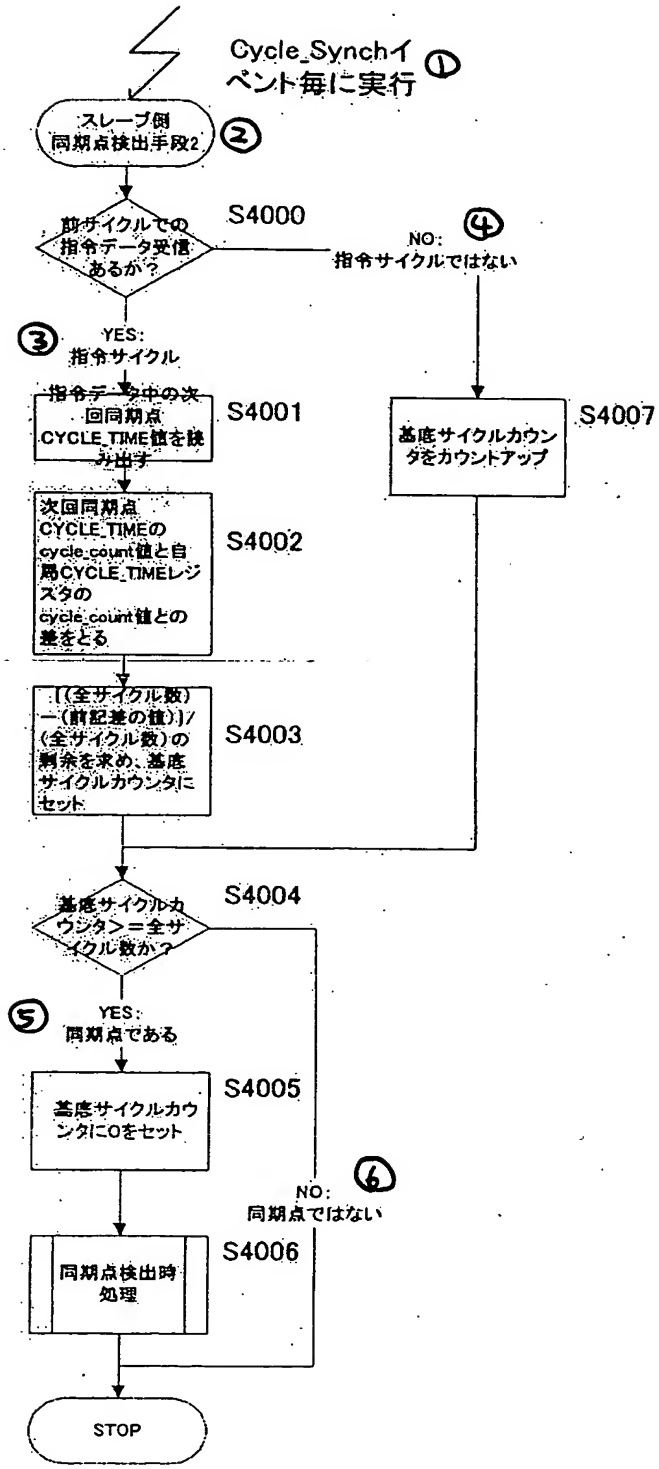

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Fig. 10

~~[図10]~~

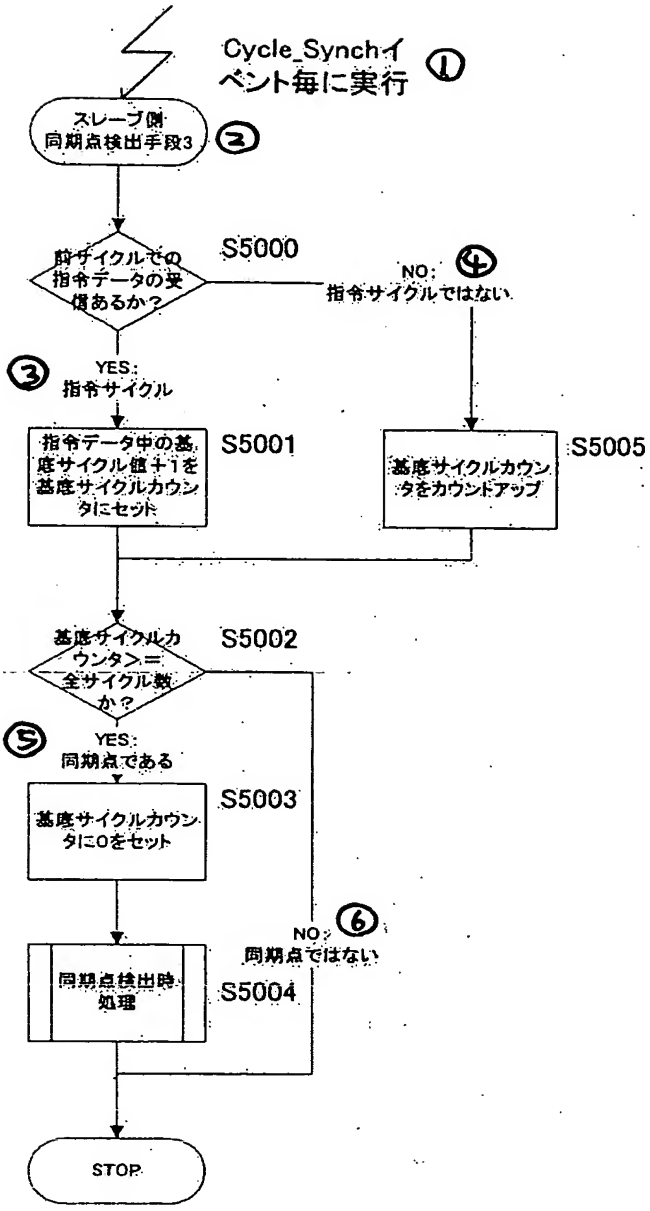

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Fig. 11
~~[図11]~~

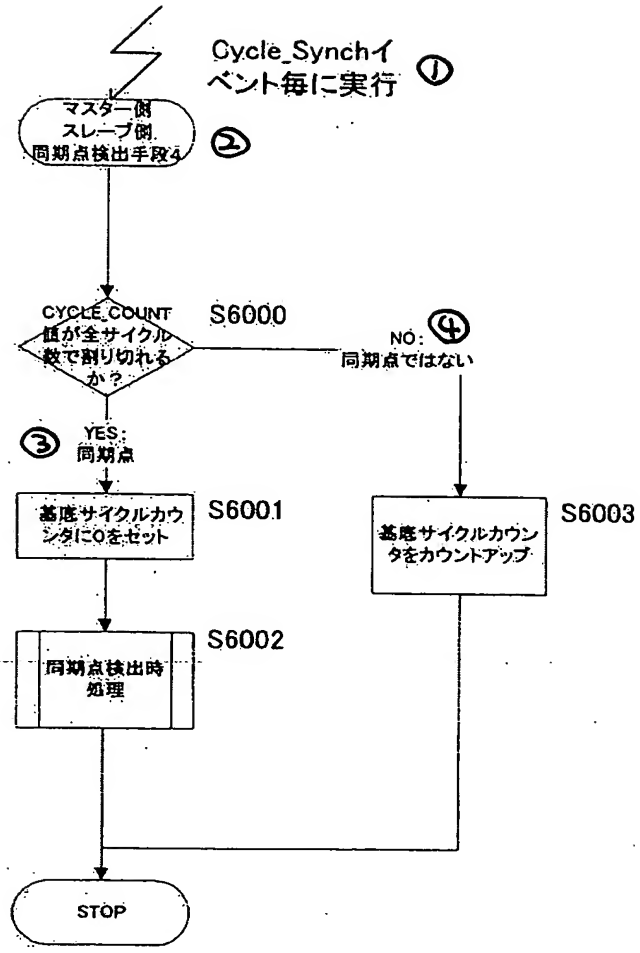

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Fig. 12

~~[図12]~~

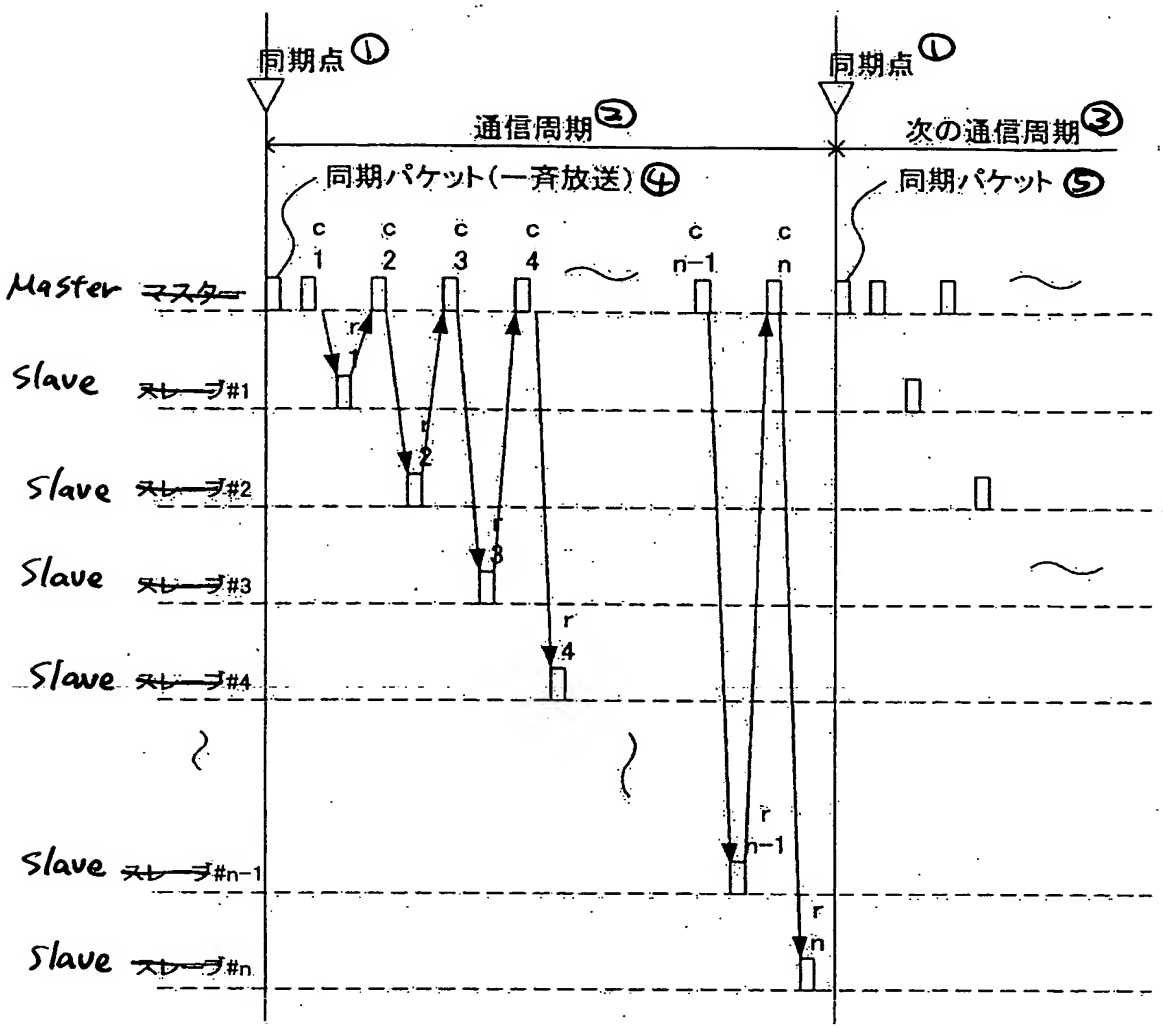

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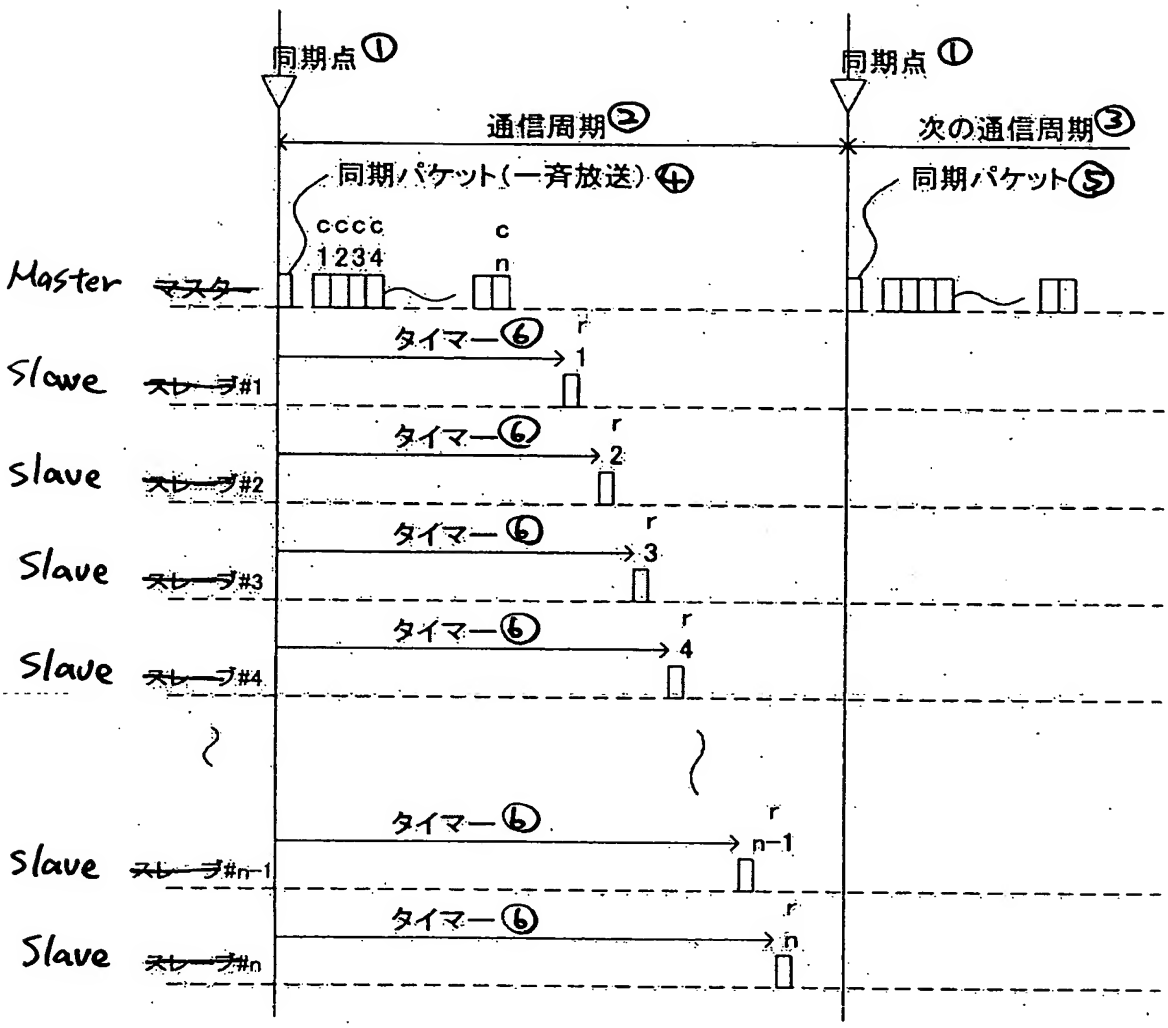
Fig. 13

~~[図13]~~